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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete If Known

Application Number	09/802,289
Filing Date	March 8, 2001
First Named Inventor	Saulsbury, Ashley
Group Art Unit	2131
Examiner Name	
Attorney Docket Number	016747-010010US

Sheet 3 of

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

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125	A	NUMOMURA Y ET AL: "M32RD-INTEGRATING DRAM AND MICROPROCESSOR" IEEE MICRO, IEEE INC. NEW YORK, US, vol. 17 no. 8, 1 November 1997 (1997-11-01), pages 40-48, XP000726003; ISSN: 0272-1732.	
125	B	KOZYRAKIS C E ET AL: "SCALABLE PROCESSORS IN THE BILLION-TRANSISTOR ERA: IRAM" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH, CA, US, US vol. 20, no. 0 1 September 1997 (1997-09-01), pages 75- 78, XP000730003; ISSN: 0018-9182.	
125	C	HERRMANN KLAUS, HILGENSTOCK JOERG, PIRSCH PETER: "ARCHITECTURE OF A MULTIPROCESSOR SYSTEM WITH EMBEDDED DRAM FOR LARGE AREA INTEGRATION" 8 October 1997 (1997-10-08), IEEE INTERNATIONAL CONFERENCE ON INNOVATIVE SYSTEMS IN SILICON, PISCATAWAY, NJ, USA; XP002178990	

DE 7056975 v1

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		Number Kind Code ² (if known)			
AS	AA	US-5,710,907	1/1998	Hagersten et al.	RECEIVED NOV 13 2003 Technology Center 2800
	AB	US-5,953,738	9/1999	Rao	
	AC	US-6,000,007	12/1999	Leung et al.	
	AD	US-6,202,143	3/2001	Rim	
	AE	US-6,128,700	10/2000	Hsu et al.	
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	AH	US-6,321,318	11/2001	Baltz et al.	
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